

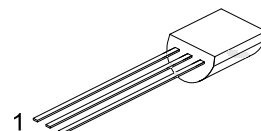
**02N60-CB****Power MOSFET****0.2A, 600V N-CHANNEL
POWER MOSFET****DESCRIPTION**

The UTC **02N60-CB** is a high voltage power MOSFET and is designed to have better characteristics, such as fast switching time, low gate charge, low on-state resistance and have a high rugged avalanche characteristics. This power MOSFET is usually used at high speed switching applications in power supplies, PWM motor controls, high efficient AC to DC converters and bridge circuits.

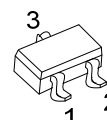
FEATURES

* $R_{DS(on)} \leq 38 \Omega$ @ $V_{GS}=10V$, $I_D=0.1A$

* High breakdown voltage



TO-92

SOT-23-3
(JEDEC TO-236)**ORDERING INFORMATION**

Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
02N60L-CB-AE2-R	02N60G-CB-AE2-R	SOT-23-3	G	S	D	Tape Reel
02N60L-CB-T92-B	02N60G-CB-T92-B	TO-92	G	D	S	Tape Box
02N60L-CB-T92-K	02N60G-CB-T92-K	TO-92	G	D	S	Bulk

Note: Pin Assignment: G: Gate D: Drain S: Source

02N60G-CB-AE2-R (1) Packing Type (2) Package Type (3) Version Code (4) Green Package	(1) R: Tape Reel, B: Tape Box, K: Bulk (2) AE2: SOT-23-3, T92: TO-92 (3) Version CB (4) G: Halogen Free and Lead Free, L: Lead Free
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MARKING

SOT-23-3	TO-92

■ **ABSOLUTE MAXIMUM RATINGS** ($T_c = 25^\circ\text{C}$, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V_{DS}	600	V
Gate-Source Voltage		V_{GS}	± 30	V
Drain Current	Continuous	I_D	0.2	A
	Pulsed	I_{DM}	0.4	A
Peak Diode Recovery dv/dt (Note 3)		dv/dt	3.3	V/ns
Power Dissipation	SOT-23-3	P_D	0.5	W
	TO-92		0.78	W
Junction Temperature		T_J	150	$^\circ\text{C}$
Storage Temperature Range		T_{STG}	$-55 \sim +150$	$^\circ\text{C}$

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating: Pulse width limited by maximum junction temperature

3. $I_{SD} \leq 0.2\text{A}$, $di/dt \leq 200\text{A}/\mu\text{s}$, $V_{DD} \leq BV_{DS}$, Starting $T_J = 25^\circ\text{C}$

■ **THERMAL DATA**

PARAMETER		SYMBOL	RATINGS	UNIT
Junction to Ambient	SOT-23-3	θ_{JA}	312	$^\circ\text{C}/\text{W}$
	TO-92		180	$^\circ\text{C}/\text{W}$
Junction to Case	SOT-23-3	θ_{JC}	250	$^\circ\text{C}/\text{W}$
	TO-92		160	$^\circ\text{C}/\text{W}$

Note: Device mounted on FR-4 substrate P_C board, 2oz copper, with 1inch square copper plate.

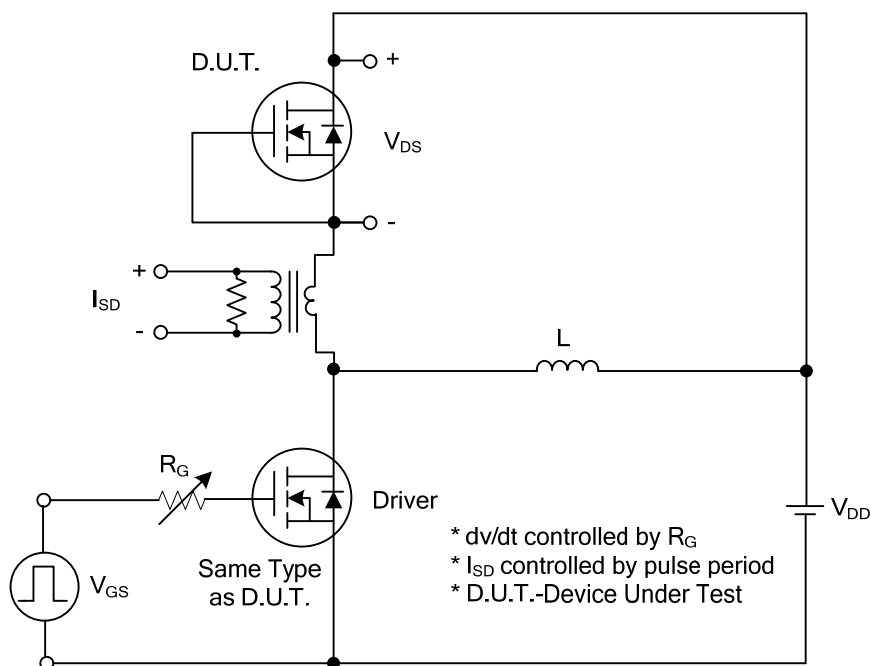
■ **ELECTRICAL CHARACTERISTICS** ($T_A = 25^\circ\text{C}$, unless otherwise specified)

PARAMETER		SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS							
Drain-Source Breakdown Voltage		BV _{DSS}	I _D =250μA, V _{GS} =0V	600			V
Drain-Source Leakage Current		I _{DSS}	V _{DS} =600V, V _{GS} =0V, T _A =25°C			10	μA
Gate-Source Leakage Current	Forward	I _{GSS}	V _{GS} =+30V, V _{DS} =0V			+100	nA
	Reverse		V _{GS} =-30V, V _{DS} =0V			-100	nA
ON CHARACTERISTICS							
Gate Threshold Voltage		V _{GS(TH)}	V _{DS} =V _{GS} , I _D =250μA	2.0		4.0	V
Static Drain-Source On-State Resistance		R _{DS(ON)}	V _{GS} =10V, I _D =0.1A			38	Ω
DYNAMIC PARAMETERS							
Input Capacitance		C _{ISS}	V _{GS} =0V, V _{DS} =25V, f=1.0MHz		36		pF
Output Capacitance		C _{OSS}			11		pF
Reverse Transfer Capacitance		C _{RSS}			3		pF
SWITCHING PARAMETERS							
Total Gate Charge		Q _G	V _{DS} =480V, V _{GS} =10V, I _D =0.2A (Note1,2)		8		nC
Gate to Source Charge		Q _{GS}			3		nC
Gate to Drain Charge		Q _{GD}			1		nC
Turn-ON Delay Time		t _{D(ON)}	V _{DS} =100V, V _{GS} =10V, I _D =0.2A, R _G =25Ω (Note1,2)		5		ns
Rise Time		t _R			20		ns
Turn-OFF Delay Time		t _{D(OFF)}			29		ns
Fall-Time		t _F			161		ns
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS							
Maximum Body-Diode Continuous Current		I _S				0.2	A
Maximum Body-Diode Pulsed Current		I _{SM}				0.4	A
Drain-Source Diode Forward Voltage		V _{SD}	I _S =0.2A, V _{GS} =0V			1.4	V
Reverse Recovery Time		t _{rr}	V _{GS} =0V, I _S =0.2A, di/dt=100A/μs		106		ns
Reverse Recovery Charge		Q _{rr}			81		nC

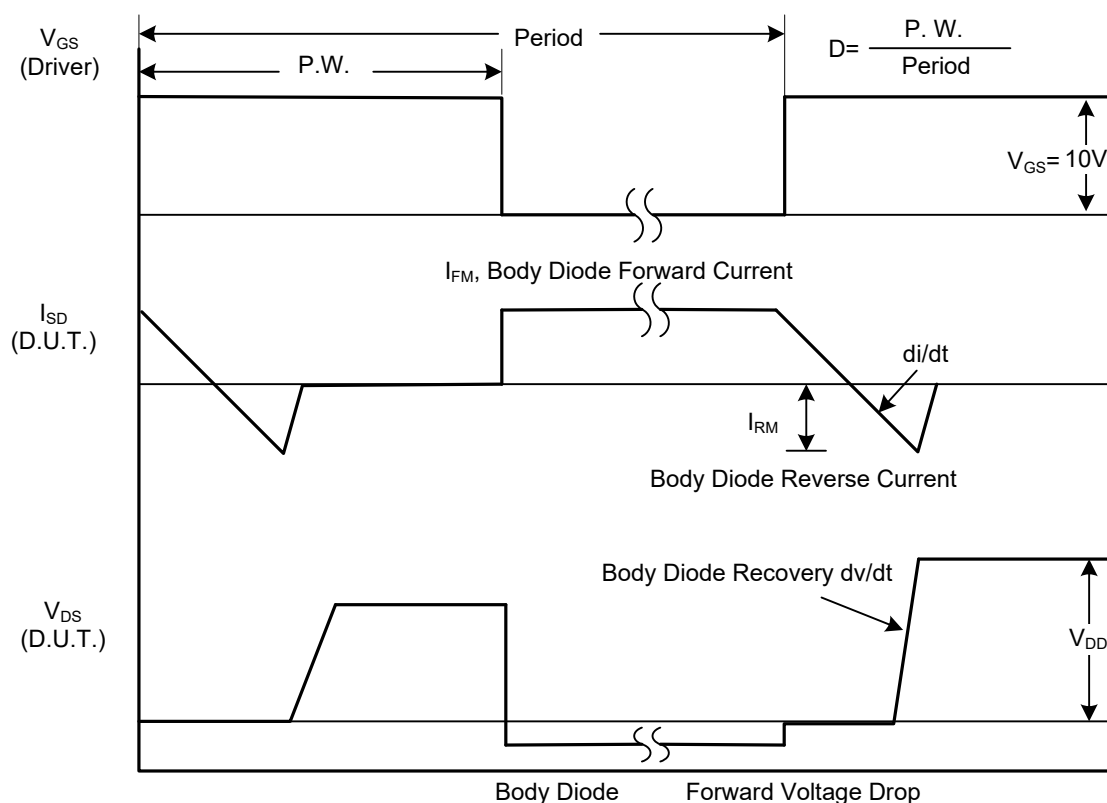
Notes: 1. Pulse Test: Pulse Width $\leq 300\mu\text{s}$, Duty Cycle $\leq 2\%$

2. Essentially independent of operating temperature.

■ TEST CIRCUITS AND WAVEFORMS

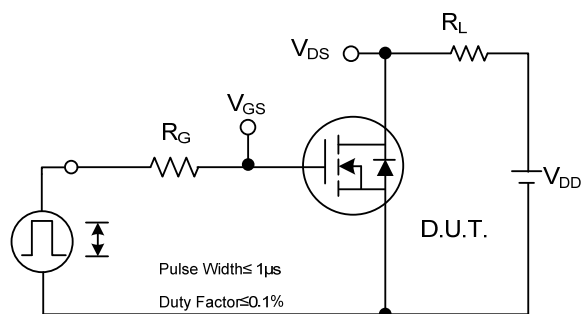


Peak Diode Recovery dv/dt Test Circuit

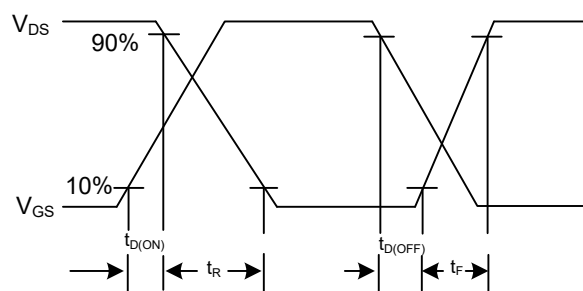


Peak Diode Recovery dv/dt Waveforms

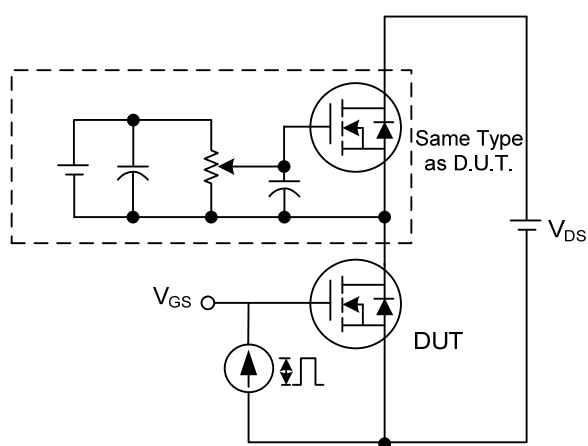
■ TEST CIRCUITS AND WAVEFORMS



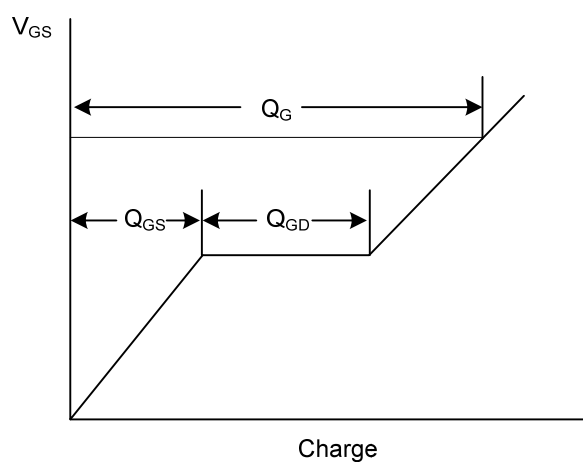
Switching Test Circuit



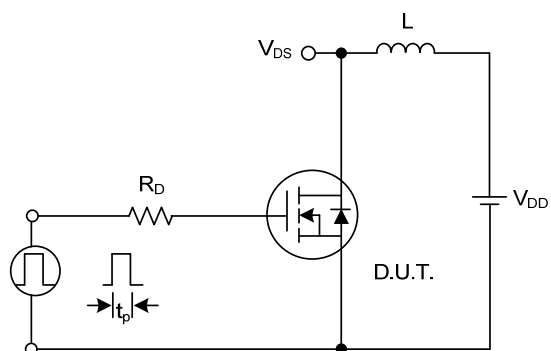
Switching Waveforms



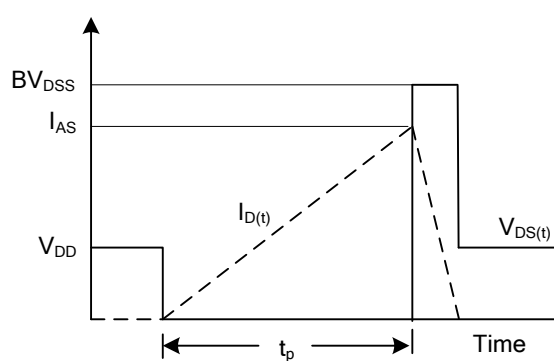
Gate Charge Test Circuit



Gate Charge Waveform

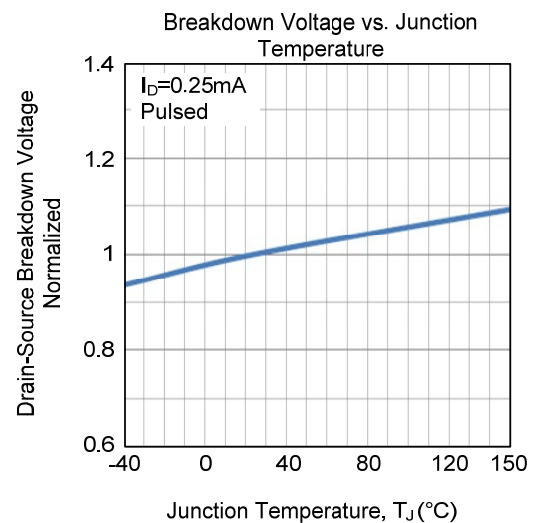
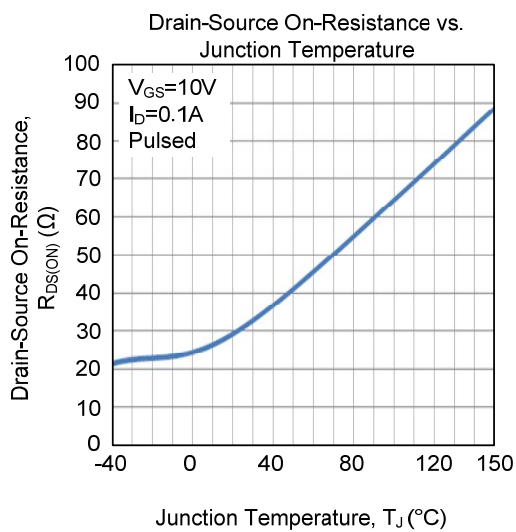
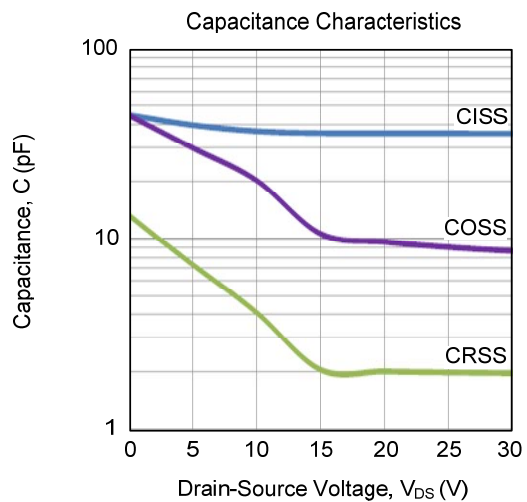
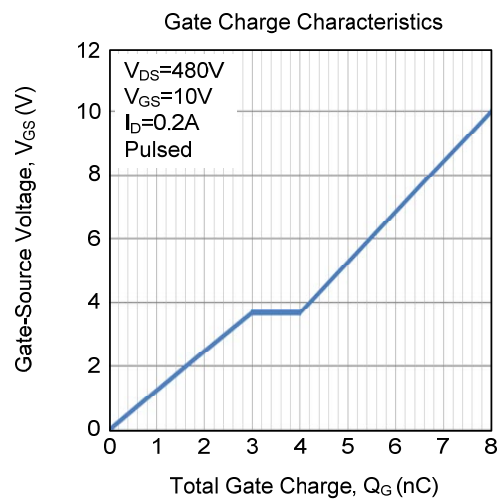
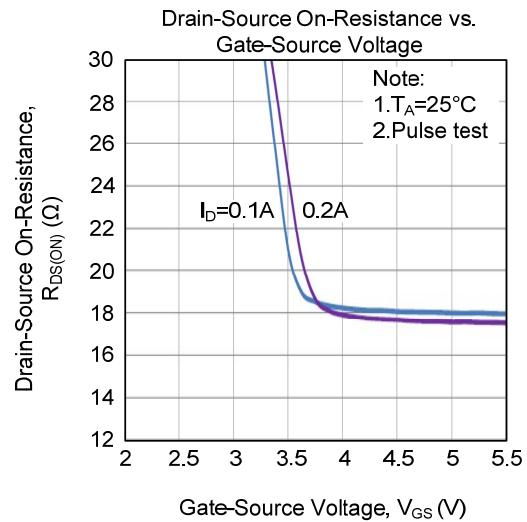
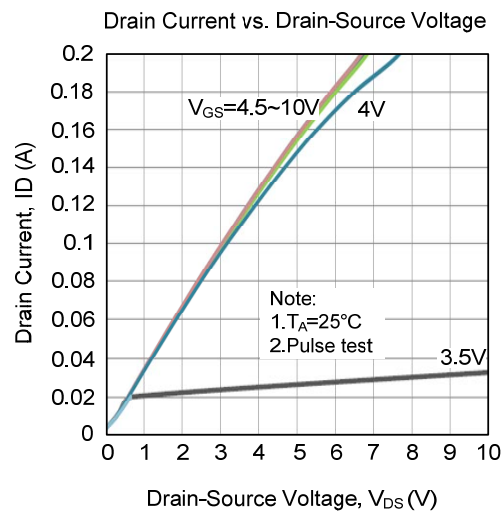


Unclamped Inductive Switching Test Circuit

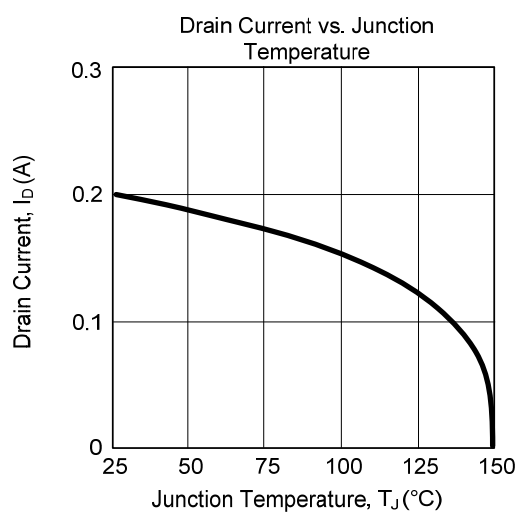
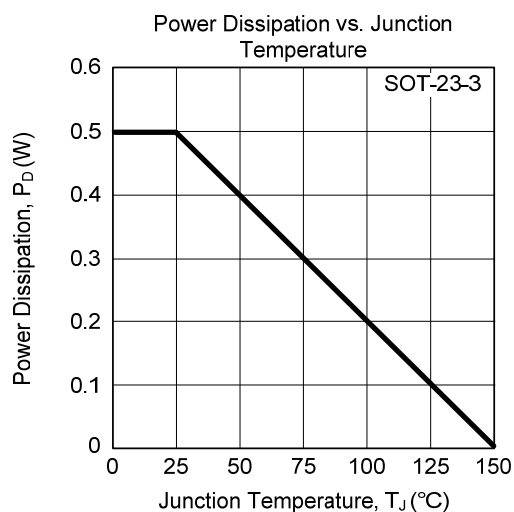
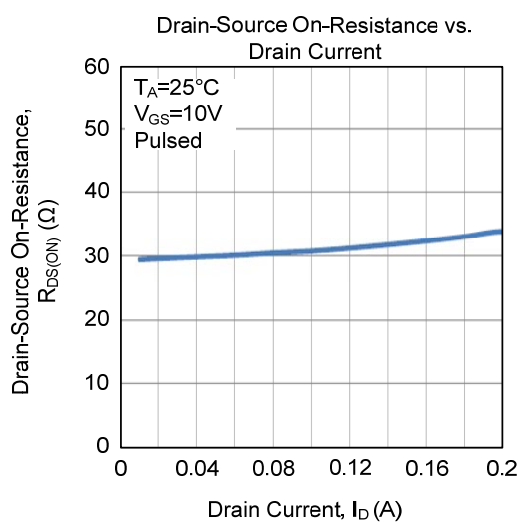
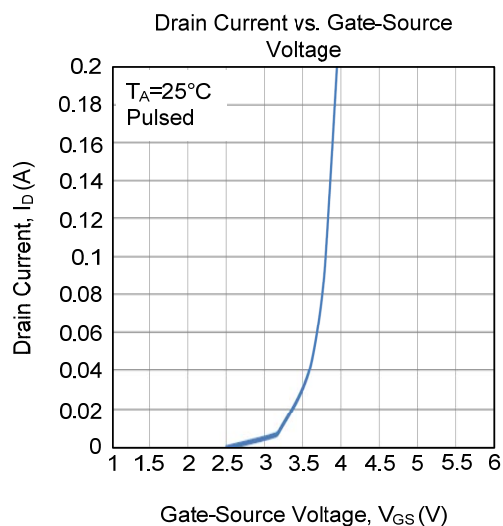
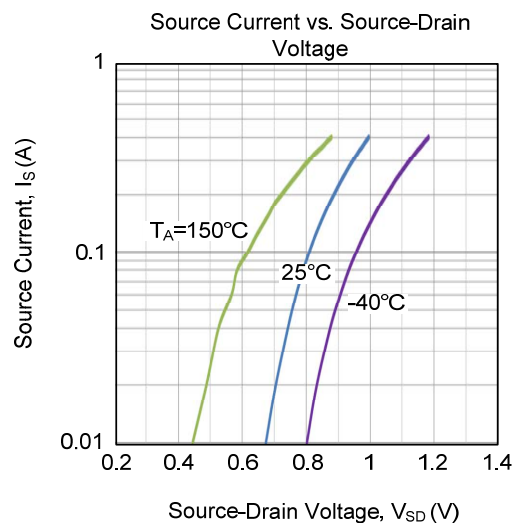
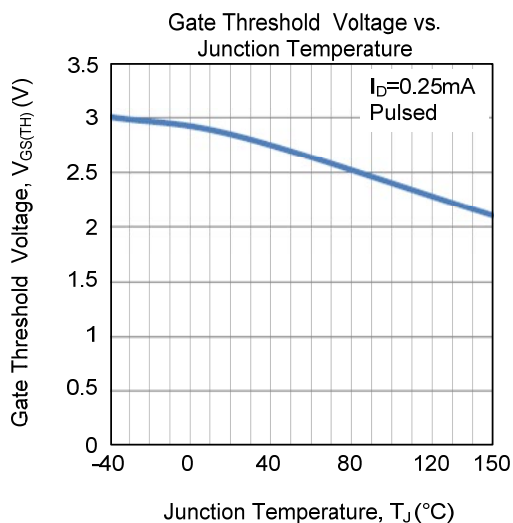


Unclamped Inductive Switching Waveforms

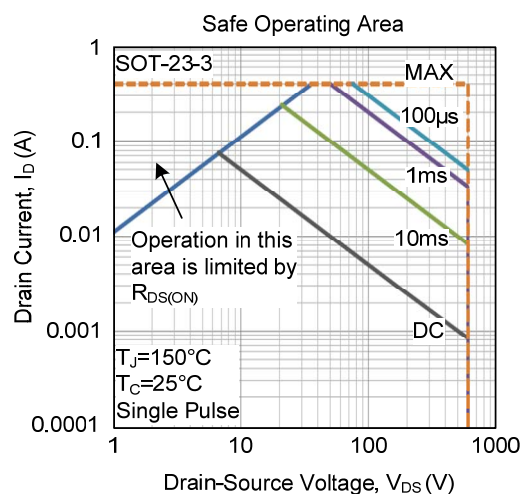
■ TYPICAL CHARACTERISTICS



TYPICAL CHARACTERISTICS (Cont.)



■ TYPICAL CHARACTERISTICS (Cont.)



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