



10N65-ML

Power MOSFET

10A, 650V N-CHANNEL POWER MOSFET

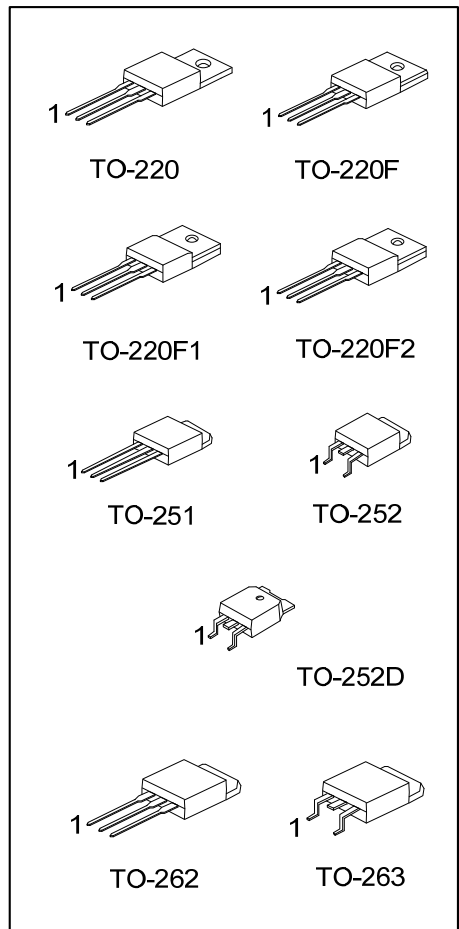
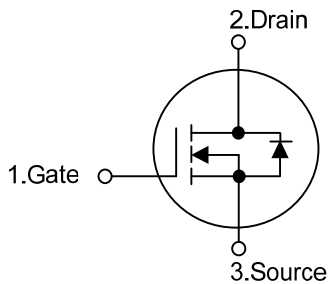
DESCRIPTION

The UTC **10N65-ML** is a high voltage power MOSFET combines advanced planar MOSFET designed to have better characteristics, such as fast switching time, low gate charge, low on-state resistance and high rugged avalanche characteristics. This power MOSFET is usually used in high speed switching applications of switching power supplies and adaptors.

FEATURES

- * $R_{DS(ON)} \leq 1.0 \Omega @ V_{GS}=10V, I_D=5.0A$
- * Fast switching capability
- * Avalanche energy tested
- * Improved dv/dt capability, high ruggedness

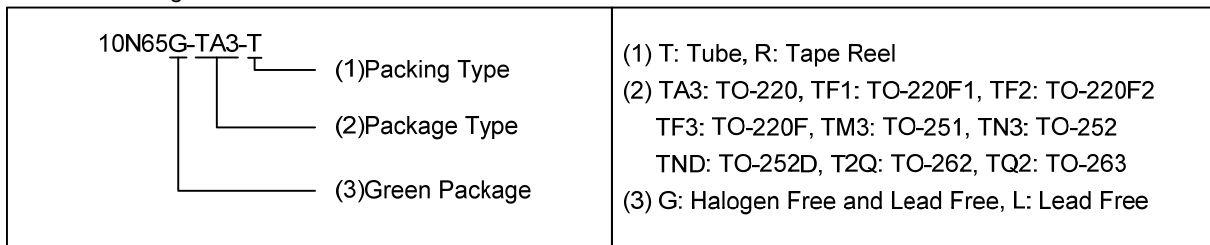
SYMBOL



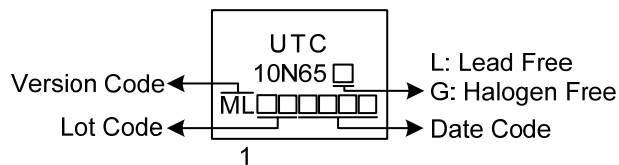
ORDERING INFORMATION

Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
10N65L-TA3-T	10N65G-TA3-T	TO-220	G	D	S	Tube
10N65L-TF1-T	10N65G-TF1-T	TO-220F1	G	D	S	Tube
10N65L-TF2-T	10N65G-TF2-T	TO-220F2	G	D	S	Tube
10N65L-TF3-T	10N65G-TF3-T	TO-220F	G	D	S	Tube
10N65L-TM3-T	10N65G-TM3-T	TO-251	G	D	S	Tube
10N65L-TN3-R	10N65G-TN3-R	TO-252	G	D	S	Tape Reel
10N65L-TND-R	10N65G-TND-R	TO-252D	G	D	S	Tape Reel
10N65L-T2Q-T	10N65G-T2Q-T	TO-262	G	D	S	Tube
10N65L-T2Q-R	10N65G-T2Q-R	TO-262	G	D	S	Tape Reel
10N65L-TQ2-T	10N65G-TQ2-T	TO-263	G	D	S	Tube
10N65L-TQ2-R	10N65G-TQ2-R	TO-263	G	D	S	Tape Reel

Note: Pin Assignment: G: Gate D: Drain S: Source



MARKING



■ ABSOLUTE MAXIMUM RATINGS (T_c=25°C, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V _{DSS}	650	V
Gate-Source Voltage		V _{GSS}	±30	V
Continuous Drain Current		I _D	10	A
Pulsed Drain Current (Note 2)		I _{DM}	30	A
Avalanche Energy	Single Pulsed (Note 3)	E _{AS}	800	mJ
Peak Diode Recovery dv/dt (Note 4)		dv/dt	2.1	V/ns
Power Dissipation	TO-220/TO-262 TO-263	P _D	135	W
	TO-220F/TO-220F1 TO-220F2		38	W
	TO-251/TO-252 TO-252D		55	W
Junction Temperature		T _J	+150	°C
Storage Temperature		T _{STG}	-55 ~ +150	°C

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating: Pulse width limited by maximum junction temperature.

3. L = 100mH, I_{AS} = 4.1A, V_{DD} = 50V, R_G = 25 Ω, Starting T_J = 25°C

4. I_{SD} ≤ 10A, di/dt ≤ 200A/μs, V_{DD} ≤ BV_{DSS}, Starting T_J = 25°C

■ THERMAL DATA

PARAMETER		SYMBOL	RATINGS	UNIT
Junction to Ambient	TO-220/TO-220F TO-220F1/TO-220F2 TO-262/TO-263	θ _{JA}	62.5	°C/W
	TO-251/TO-252 TO-252D		110	°C/W
Junction to Case	TO-220/TO-262 TO-263	θ _{JC}	0.92	°C/W
	TO-220F/TO-220F1 TO-220F2		3.29	°C/W
	TO-251/TO-252 TO-252D		2.27 (Note)	°C/W

Note: Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.

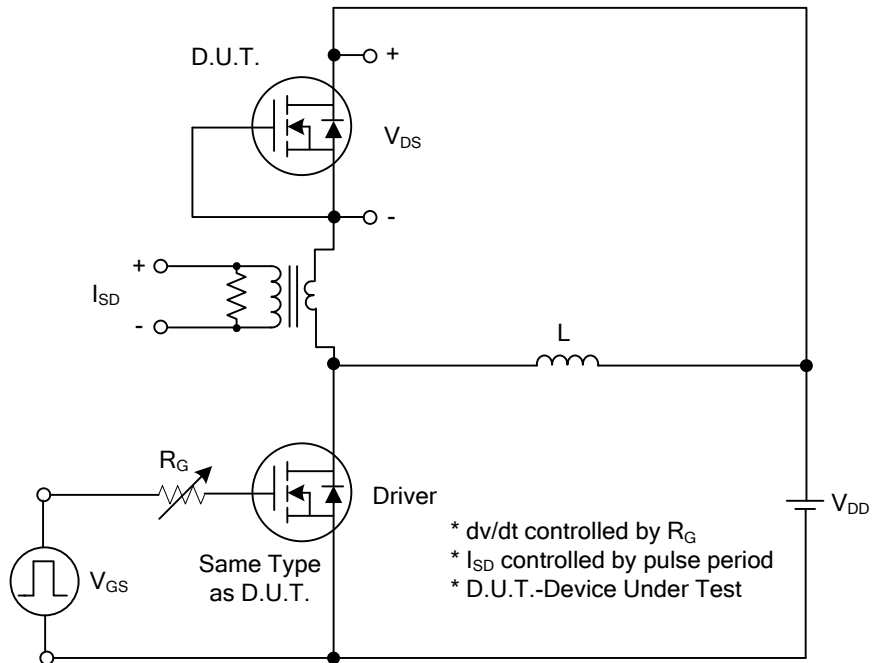
■ ELECTRICAL CHARACTERISTICS (T_J=25°C, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _D =250μA	650			V
Drain-Source Leakage Current	I _{DSS}	V _{DS} =650V, V _{GS} =0V			10	μA
Gate- Source Leakage Current	Forward	I _{GSS} V _{GS} =30V, V _{DS} =0V			100	nA
	Reverse		V _{GS} =-30V, V _{DS} =0V			-100
ON CHARACTERISTICS						
Gate Threshold Voltage	V _{GS(TH)}	V _{DS} =V _{GS} , I _D =250μA	2.0		4.0	V
Static Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =10V, I _D =5.0A			1.0	Ω
DYNAMIC CHARACTERISTICS						
Input Capacitance	C _{ISS}	V _{DS} =25V, V _{GS} =0V, f=1.0MHz		1300		pF
Output Capacitance	C _{OSS}			124		pF
Reverse Transfer Capacitance	C _{RSS}			9.3		pF
SWITCHING CHARACTERISTICS						
Total Gate Charge (Note 1)	Q _G	V _{DS} =520V, V _{GS} =10V, I _D =10A I _G =1mA (Note 1, 2)		31		nC
Gate-Source Charge	Q _{GS}			7.6		nC
Gate-Drain Charge	Q _{GD}			5.8		nC
Turn-On Delay Time (Note 1)	t _{D(ON)}	V _{DS} =100V, V _{GS} =10V, I _D =10A, R _G =25Ω (Note 1, 2)		20		ns
Turn-On Rise Time	t _R			21		ns
Turn-Off Delay Time	t _{D(OFF)}			98		ns
Turn-Off Fall Time	t _F			35		ns
DRAIN-SOURCE DIODE CHARACTERISTICS AND MAXIMUM RATINGS						
Drain-Source Diode Forward Voltage (Note 1)	V _{SD}	I _S =10A, V _{GS} =0V			1.4	V
Reverse Recovery Time (Note 1)	t _{rr}	I _S =10A, V _{GS} =0V di/dt=100A/μs		376		ns
Reverse Recovery Charge	Q _{rr}				8.5	

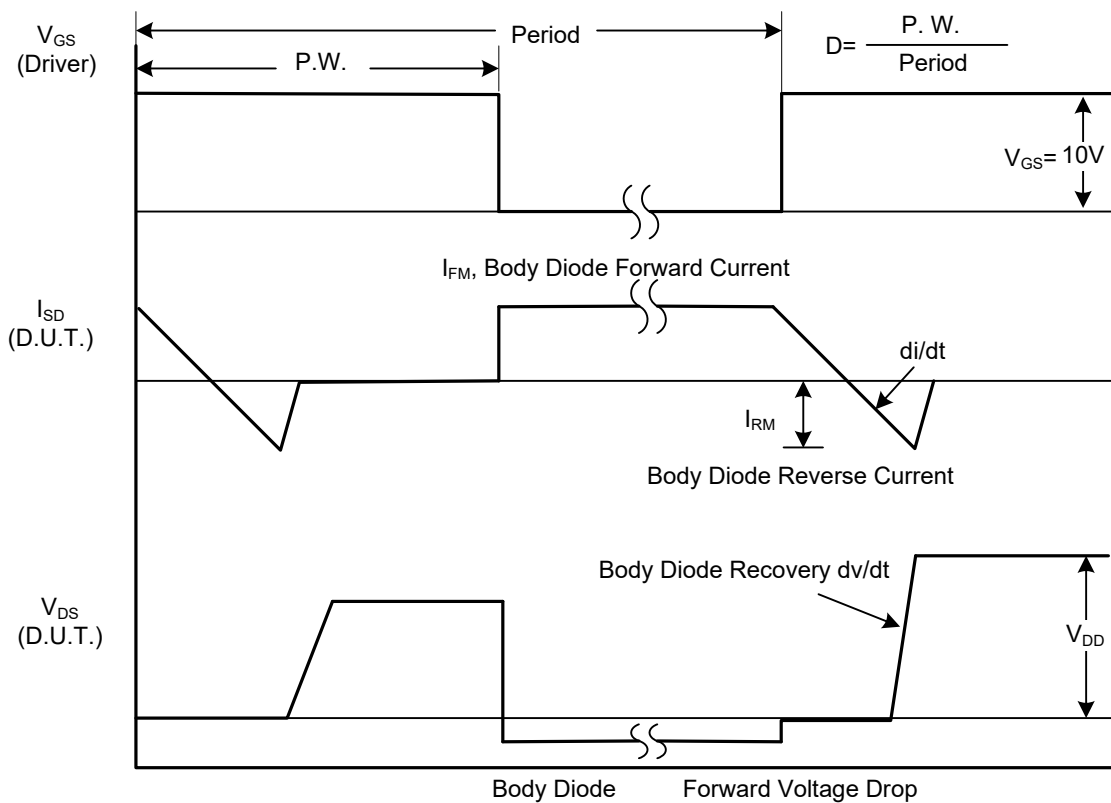
Notes: 1. Pulse Test: Pulse width ≤ 300μs, Duty cycle ≤ 2%.

2. Essentially independent of operating temperature.

TEST CIRCUITS AND WAVEFORMS

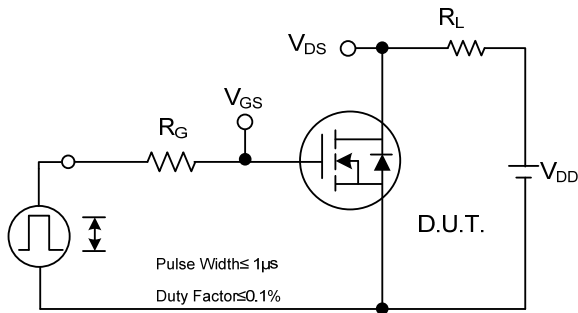


Peak Diode Recovery dv/dt Test Circuit

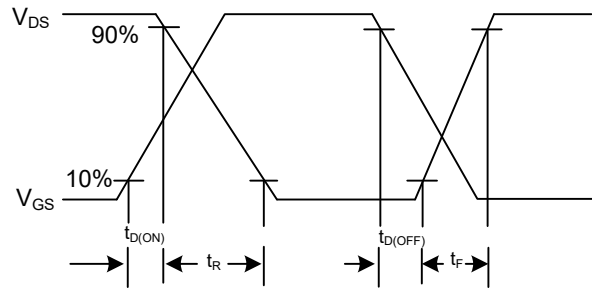


Peak Diode Recovery dv/dt Waveforms

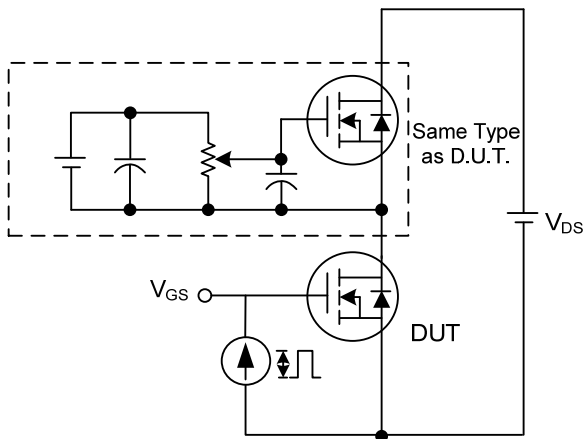
TEST CIRCUITS AND WAVEFORMS



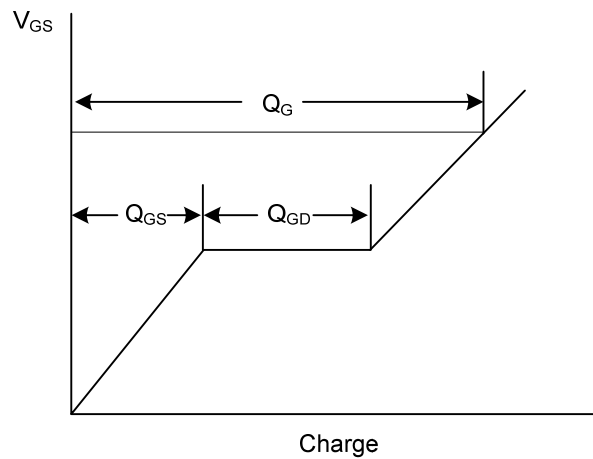
Switching Test Circuit



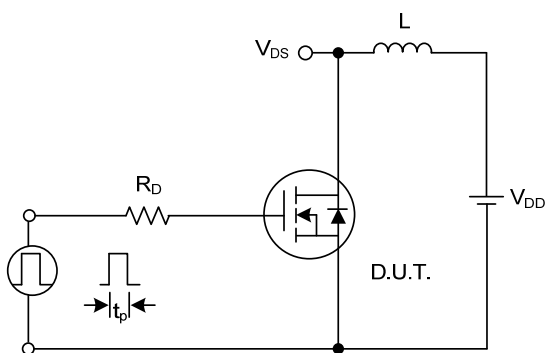
Switching Waveforms



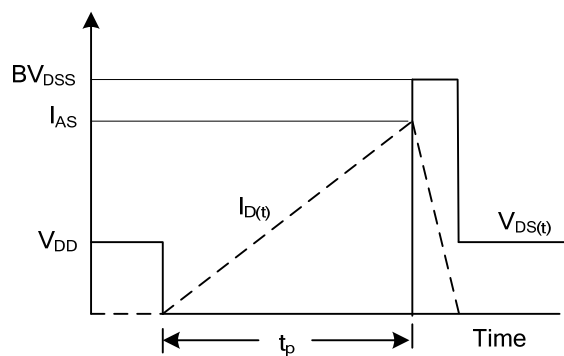
Gate Charge Test Circuit



Gate Charge Waveform

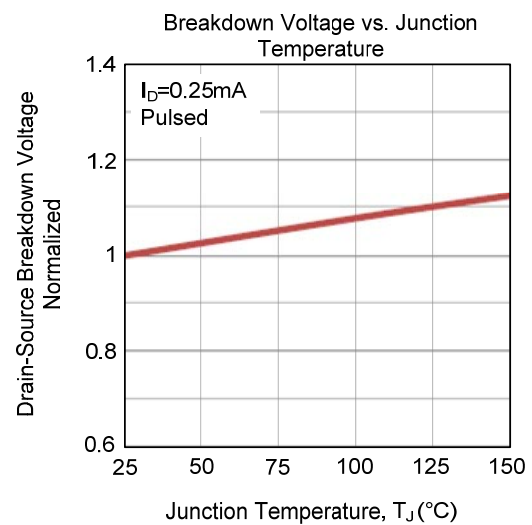
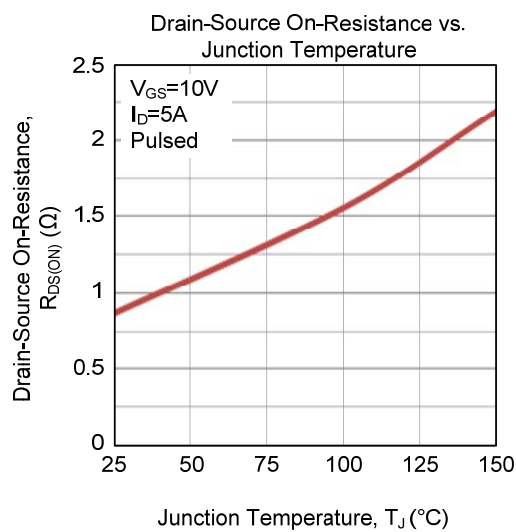
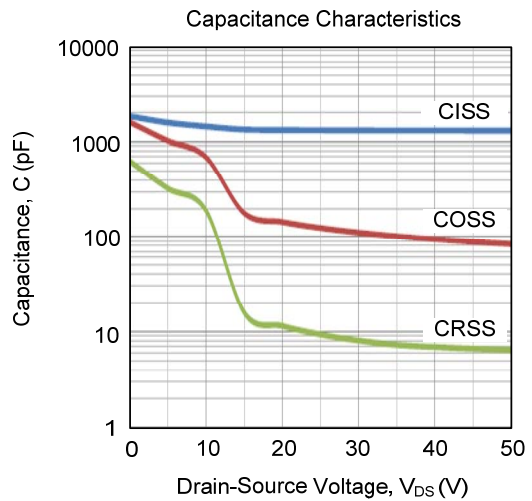
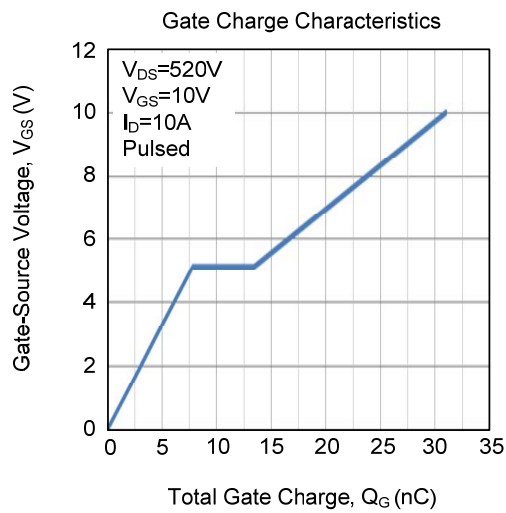
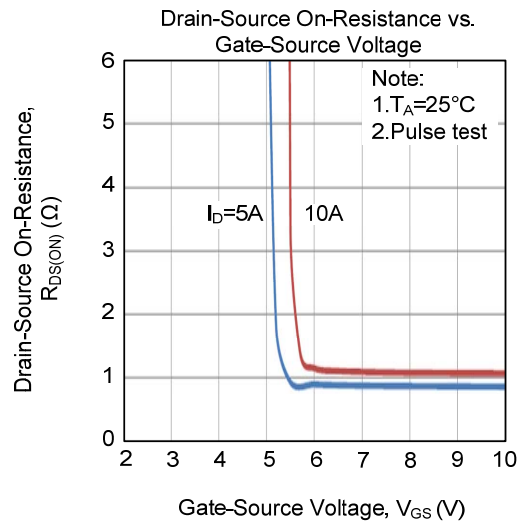
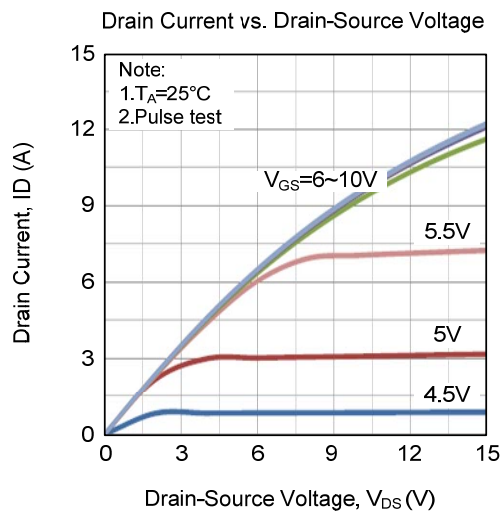


Unclamped Inductive Switching Test Circuit

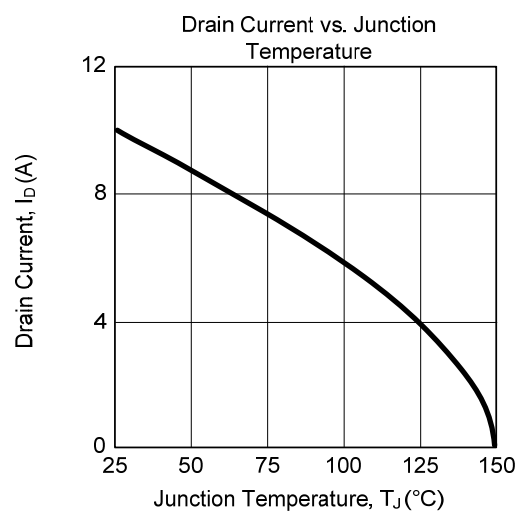
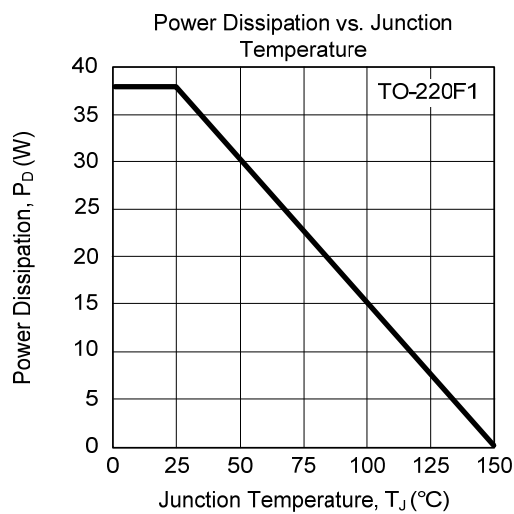
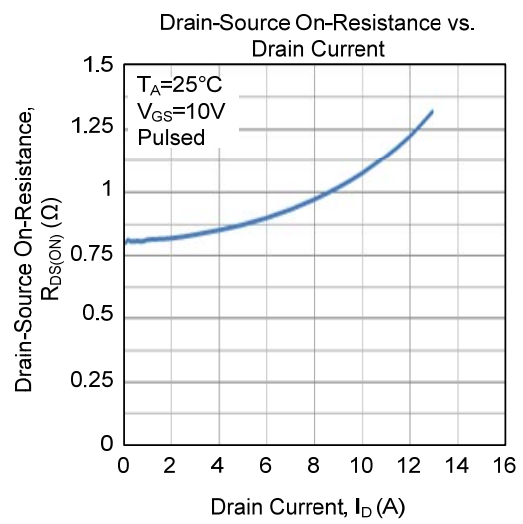
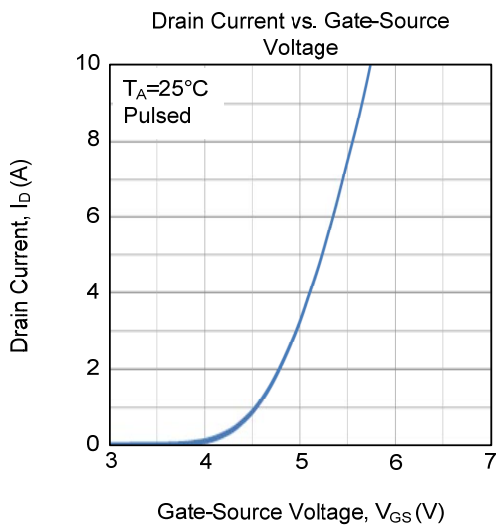
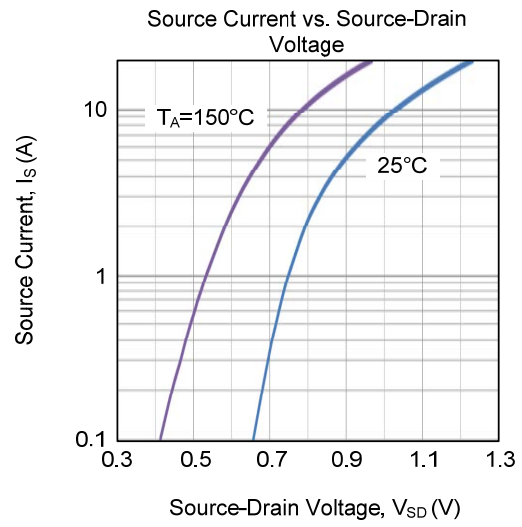
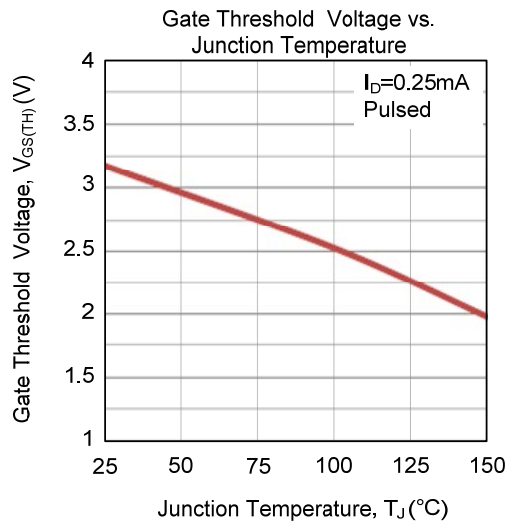


Unclamped Inductive Switching Waveforms

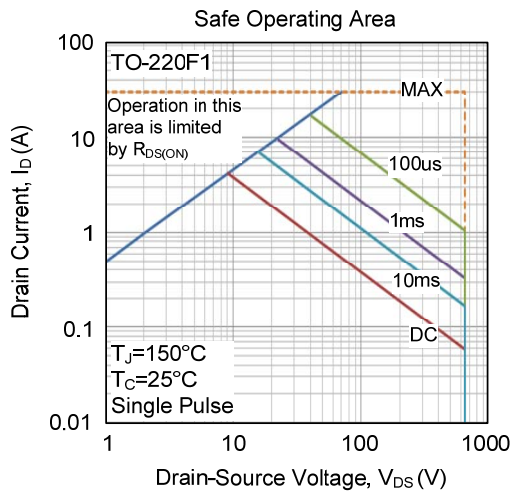
TYPICAL CHARACTERISTICS



■ TYPICAL CHARACTERISTICS (Cont.)



■ TYPICAL CHARACTERISTICS (Cont.)



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